

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Jeffrey M. Mason et al.

Assignee: Xilinx, Inc.

Title: "Modular Design Method and System for Programmable Logic Devices"

Serial No.: 09/839,464

File Date: 04-20-01

Examiner: Unknown

Art Unit: 2123

Docket No.: X-658 US

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

#3
Prior Art
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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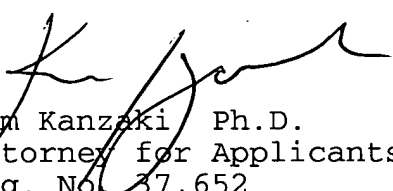
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the six (6) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action


Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,


Kim Kanzaki Ph.D.
Attorney for Applicants
Reg. No. 37,652

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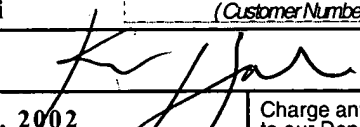
TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> BOX: NON-FEE AMENDMENT Express Mail Receipt No.	Application / Conf. No.	09/839,464 / 2465
	Filing Date	April 20, 2001
	First Named Inventor	Jeffrey M. Mason
	Examiner Name	Unknown
	Group Art Unit	2123
	Issue Fee Batch No.	
	Attorney Docket Number	X-658 US

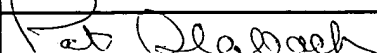
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Firm or Customer Number	24309 (Customer Number)	Reg. Number 37,652
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Date	December 9, 2002	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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